AXIS 88 Developer Board AXIS 89 Device Server

Flexible and secure industry device server platforms

AXIS 88 Developer Board and AXIS 89 Device Server are the developer enabler and deployment platform for AXIS ETRAX FS System-on-Chip. The ideal complete deployment products for networking devices even in rugged environments.



HARDWARE ACCELERATED ENCRYPTION ≥

Hardware accelerated wire-speed cryptography with supported crypto algorithms AES, DES and 3DES and hashing algorithms SHA-1 and MD-5 to build secure products.

STORAGE CAPABILITIES ≥

Connect additional flash memory using the MMC slot (Multi Media Card) or use the 28 MB already built-in for your application use.

RISC PROCESSOR ≥

200 MIPS 32-bit RISC processor with low power consumption.

FLEXIBLE INTERFACE ≥

Versatile and flexible interface possibilities to fit any communication need, e.g. dual 100 Mbit/s Ethernet, serial ports, USB and digital I/O ports.



TECHNICAL SPECIFICATIONS - AXIS 88 DEVELOPER BOARD/AXIS 89 DEVICE SERVER

Models		AXIS 88 Developer Board AXIS 89 Device Server				
Ethernet network ports	Dua	Dual 10/100 Mbit/s Ethernet ports				
Serial connection	Three RS-232 serial ports. Support RXD, TXD, RTS, CTS, DSR, DTR, RI and CD at 230 kBaud (maximum speed supported by transceiver chips)					
	Pin	out:				
	1	CD	Carrier detect		Input	
	2	RxD	Receive data		Input	
	3	TxD	Transmit data		Output	
	4	DTR	Data terminal read	dy	Output	
	5	GND	Ground			
	6	DSR	Data set ready		Input	
	7	RTS	Request to send		Output	
	8	CTS	Clear to send		Input	
	9	RI	Ring indicator		Input	
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USB 1.1 port	Compliant USB 1.1 host port for connection to web camera: barcode scanners, Bluetooth, wireless adapater, etc. The port can provide +5.0 V DC, 500 mA to the device			
System/application management and recovery	Linux system and applications can be uploaded remotely to the device server over the network and stored in flash memory Complete system recovery is made easy using Axis supplied software utility.			
Hardware	CPU: 200 MIPS, 32 bit RISC processor (AXIS ETRAX FS) Flash memory: 32 MB flash (28 MB available for applications in default configuration) RAM: 32 MB			
Additional memory	Connect external flash memory using the MMC slot (Multi Media Card)			
Hardware encryption	Hardware accelerated wire-speed cryptography to build secure products Supported crypto algorithms are AES, DES and 3DES Supported hashing algorithms are SHA-1 and MD-5 Throughput 200 - 3000 Mbit/s			
Power supply	Power: 8-20 V DC on terminal block, via external power supply AXIS PS-K 9 V DC / 1 A (included) Power consumption typically 2 W at 8-20 V DC			
Operating conditions	5 - 50 °C Humidity 8 - 80% RHG, non-condensing			
Temperature sensor	The temperature sensor provides reliable temperature readouts at a resolution of 0.25 °C in the interval 5 - 50 °C			
Real time clock	Real time clock with battery backup			
Product warranty	1 year			
Casing*	A stable aluminum casing that can be conveniently wall mounted (mounting brackets are included)			
Approvals (AXIS 89 Device Server compliant with both industrial and light industrial/commercial EMC standards for both emission and immunity)	Immunity standards: EN 55024, EN 61000-6-1, EN 61000-6-2 Emission Standards: EN 55022 Class B, EN 61000-3-2, EN 61000-3-3, FCC Part 15 Subpart B Class B, VCCI Class B, C-Tick AS/NZS CISPR22, ICES-003 Class B Safety: EN 60950-1, UL (US version), cUL (Canadian version) The approvals are made with the aluminum casing and cannot be guaranteed when the PCB is not mounted in a similar box*			
Dimensions (HxWxD) and weight	AXIS 88 Developer Board: 17 x 124 x 104 mm (0.7" x 4.9" x 4.1"), 130 g (0.29 lbs) AXIS 89 Device Server: 41 x 140 x 118 mm (1.6" x 5.5" x 4.6"), 445 g (1.0 lbs)			

^{*} Applies to the AXIS 89 Device Server only

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